

Accelerator Design for FAIR and Application to HIF

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New developments for the FAIR accelerator complex and the present status of operation and performance of the GSI accelerator facilities are presented and compared to the GSI HIDIF study approach. New technical solutions and developments with relevance to the RF based HIF driver scenario will be presented. Technical challenges and issues for achieving driver performance will be addressed.

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Session Classification: Accelerator physics and technology I - Chairs: J. Kwan and A. Faltens - Featured Posters: R. Burke, K. Fukushima, T. Yoshimoto, R. Cassel