

# **LBNL LLRF Meeting Agenda**

## **Wednesday Building #2 100B**

- 8:30: Architecture Overview (Larry)
- 9:00: Precision Receiver Requirements
  - Down Converter (FNAL)
  - Digitizer (LBNL)
  - FPGA/Carrier (JLAB)
  - Chassis and I/O (All)
  - Gateway/Firmware (LBNL/JLAB)
  - Software (JLAB)
- 11:00 mTCA RF Station
  - Plan to develop mTCA hardware/software (Sandeep)
  - LBNL mTCA capabilities and firmware (LBNL)
  - Down Converter/Digitizer
- 12:30 Lunch (LBL Cafeteria)
- 1:30 mTCA RF Station Cont.
  - FPGA/Carrier (Sandeep/Rama)
  - RF BP and LO distribution
  - Crate/ I/O/ICD (start of rack cabling discussion)
- 3:30 Master Oscillator/PRL (Bo)
  - PRL
    - 650 MHz
    - 3.9 GHz Reference
    - Multiple Frequencies (Ed C.)
  - LO Distribution
    - 1320 MHz
    - 3.9 GHz LO
- 5:00 mTCA RF Station Cont.
  - Crate/ I/O/ICD Cont.
  - Interlocks (Curt)
  - Software
- 6:30 Wrap Up/Schedule Adjustments for Thursday

## Thursday Building #71 264

8:30: Resonance control options (w/wo mTCA)

Piezo (Brian)

Stepper (Rama)

10:00 CMTF

FNAL (Brian)

JLAB (Curt)

LCLS2 LLRF (how it fits in to the CMTF)

11:00: mTCA Cable and Rack layout schemes

12:30: Lunch

1:30: mTCA Cable and Rack layout schemes Cont.

2:30: Wrap Up

Next steps

System Design Review (w external review panel) when?

DOE Review recommendations